



- ☐ Drafts
- ☐ Pending
- ☒ Active
 - ☐ L1: (2) ("20040015970").PN.
 - ☐ L2: (6347) adapt\$5 adj comput\$4
 - ☐ L3: (67553) memory adj controller
 - ☐ L4: (0) 711/147,149,154
 - ☐ L5: (3877) (711/147,149,154).CCLS.
 - ☐ L6: (1392) (710/22,23).CCLS.
 - ☐ L7: (346) (712/13,15).CCLS.
 - ☐ L8: (9) 2 and 3 and (5 or 6 or 7)
 - ☐ L9: (16) (2 and 3).clm.
 - ☐ L10: (13) 9 not 8
 - ☐ L11: (337) 2 and 3
 - ☐ L12: (33004) ("711").CLAS.
 - ☐ L13: (23) 11 and 12
 - ☐ L14: (20) 13 not 8
 - ☐ L15: (19) 14 not 10
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (0)
- ☐ UDC
- ☐ Queue
- ☐ Trash

Search Browse Update Clear

DBs US-PGPUB;USPAT;USOCR;FPRS;EPO;JPO;DERWENT;IBM_Y08

Default operator: OR

☒ Exact☒ Highlight all hit terms initially

14 not 10

BRS form IS&R form Hits Details